# Revision History

<table>
<thead>
<tr>
<th>SCH REV</th>
<th>PCB Rev</th>
<th>ECO No.</th>
<th>Date</th>
<th>Comment</th>
</tr>
</thead>
<tbody>
<tr>
<td>1.0</td>
<td>1.0</td>
<td></td>
<td></td>
<td>3/17/97 PRODUCTION RELEASE</td>
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<tr>
<td>1.1</td>
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<td>5/24/97</td>
<td>TCN</td>
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<tr>
<td>1.2</td>
<td>1.2</td>
<td></td>
<td>12-5-97</td>
<td>F3 AND FB CHANGE</td>
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<tr>
<td>1.3</td>
<td>1.3</td>
<td>7559</td>
<td>1-8-98</td>
<td>CHANGED R141, BC20, BC23, C27, C41, C53</td>
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<tr>
<td>1.3.1</td>
<td>1.3</td>
<td></td>
<td>1-22-98</td>
<td>CHANGED R132 FROM 20K TO 4.7K</td>
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<tr>
<td>1.3.3</td>
<td>1.3</td>
<td>7458</td>
<td>3-3-98</td>
<td>SEE ECN FOR CHANGES</td>
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<tr>
<td>1.3.4</td>
<td>1.3</td>
<td>7447</td>
<td>5-7-98</td>
<td>CHANGED R28, 29, 30, 104, 143, 144 FROM 200R TO 20R</td>
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<tr>
<td>1.3.5</td>
<td>1.3</td>
<td>8273</td>
<td>7-13-99</td>
<td>ADDED TRANSISTOR TO C34 AND OTHER ASSY Dwg CHANGES</td>
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<tr>
<td>1.4</td>
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<td></td>
<td>4-9-99</td>
<td>SEE ECN 0321 4 4435 FOR CHANGES</td>
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**Check Print** 14 Apr 99

**Designer**

NOTES:

1. ALL RESISTORS ARE IN OHMS
2. ALL RESISTORS ARE 1/8 WATT 5% OR BETTER
3. CAPACITOR VALUES ARE IN PICO FARADS
4. ALL NON POLARIZED CAPACITORS ARE 5% OR BETTER
   VALUES BELOW 470pF ARE NPO/C0G TYPE 5%
   VALUES 470pF TO 1000pF ARE X7R 10%
   VALUES ABOVE 1000pF ARE 25U +/- 20%
5. ALL POLARIZED CAPACITORS ARE 20% OR BETTER
6. ALL DIODES ARE IN4148.

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HIGH END SYSTEMS INC.

**Technobeam**

**Function** COVER SHEET

**Drawing No.** SCH-99030079 1.4

**Designed:** TOM GRIMES

**Drawn:** DRAWER

**Date:** 4-8-1999 10:00  **Size:** A2  **Page:** 3 of 13
NOTES:

1. USA & U6A ARE 128Kx8 92 PIN DEVICES.
   US & U6 ARE 32Kx8 28 PIN DEVICES.
   EITHER DEVICE MAY BE USED.
   SYMBOL FOR 32 PIN DEVICE SHOWN.
   28 PIN DEVICE DOES NOT USE PINS 1, 2, 31, 32.

2. U4 MAY BE EITHER A 512Kx8 OR 128Kx8.
   128Kx8 DEVICE DOES NOT USE PINS 3 AND 30.